I2CSlave Register Map

Device address is 0x66 (0xCC) for writes and 0x67 (0xCD) for reads.

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| --- | --- | --- | --- |
| **Register Address** | **Name** | **Description** | **R = Readable W = Writable** |
| 0x01 | TEMP | Temperature (°C) | R |
| 0x02\* | PRESS\_L | Low byte of pressure (kPa) | R |
| 0x03 | PRESS\_H | High byte of pressure (kPa) | R |
| 0x04 | SAMP\_RATE | Sample rate in 100ms increments (default = 5) | R/W |
| 0xfe | WHO\_AM\_I | Chip ID – 0x73 | R |

\*After a read of register 0x02 the register address will automatically increment to 0x03. The pressure registers can be read in a single two byte read.